

Form PTO-1449 O P E INFORMATION DISCLOSURE CITATION AN APPLICATION SEP 22 2003 (Use several sheets if necessary)	Doctoral Number (Optional) HALO-02-004	Applicant Number 10/602,228
	Applicant Ki-Tae Park et al.	Filing Date 06/24/03
		Drawn At U.S. 2828

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U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

	<p>"A 5-V-Only 0.6 μm Flash EEPROM with Row Decoder Scheme in Triple-Well Structure", IEEE Journal of Solid State Circuits, Vol. 27, No. 11, Nov. 1992, pp. 1540 - 1545.</p>
	<p>"On-Chip High-Voltage Generation in MNOS Integrated Circuits Using Improved Voltage Multiplier Technique", IEEE Journal of Solid-State Circuits, Vol. 11, No. 3, June 1976, pp. 374 - 378.</p>

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Form PTO-1449	Document Number (Serial)	Application Number
O I P E INFORMATION DISCLOSURE CITATION IN AN APPLICATION MAY 22 2003 U.S. Patent and Trademark Office (Use several sheets if necessary)	HALO-02-004	10/602,228
	Applicant	Ki-Tae Park et al.
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~~S. PATENT DOCUMENTS~~

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (including Author, Title, Date, Portion or Pages, Etc.)

"A 3.3 V-Only 16 Mb DINOR Flash Memory," IEEE International Solid State Circuits Conference, Digest of Technical Papers, 1995, pp. 122-123.

EXAMINER	DATE CONSIDERED
Sterrett	12/16/04

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